

FIG. 1A

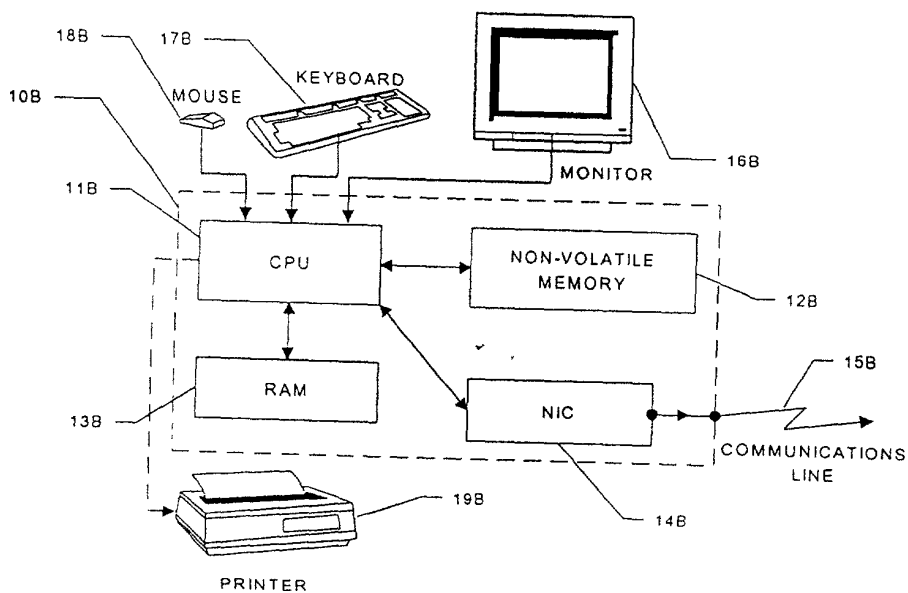
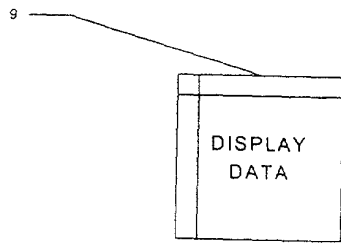


FIG. 1B



Insert memory mappings for volatile and non-volatile memory.

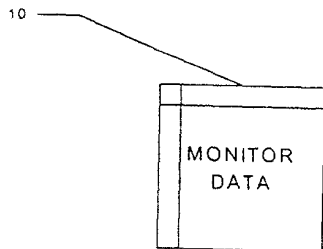


FIG. 1C

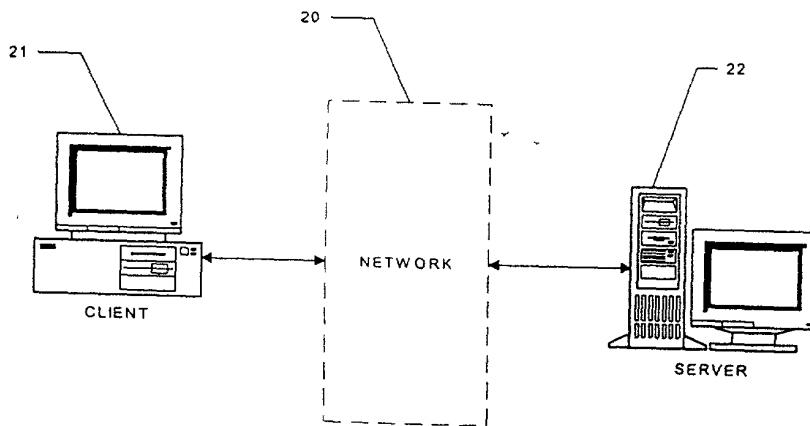


FIG. 1D

09881577 061401
TOTAL 2/518860

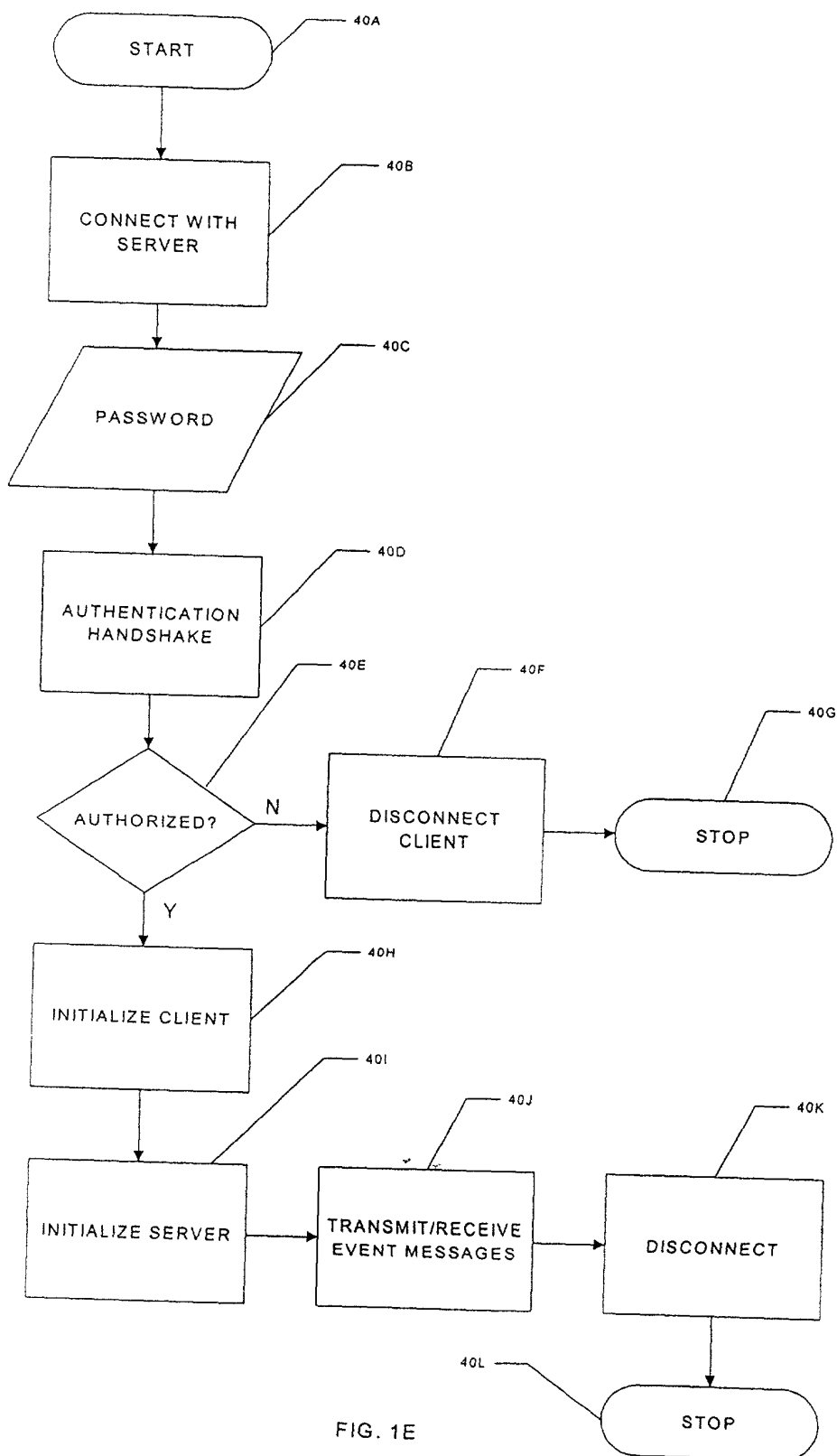


FIG. 1E